



Full Name of Institution	Universitat Politècnica de València http://www.upv.es	
Erasmus Code	E VALENCI02	
School	School of Design Engineering (ETSID) http://www.etsid.upv.es/	
Address	International Relations Office School of Design Engineering (ETSID) Universitat Politècnica de València Cno. Vera s/n 46022 Valencia, Spain Tel: +34 963877007 Ext. 81815 Tel: +34 963879185 Fax: +34 963879180 relint@etsid.upv.es	
Degrees offered at ETSID		
http://novapaginaetsid.webs.upv.es/?page_id=29 http://www.etsid.upv.es/?page_id=75		
160-Degree in Aerospace Engineering	Grado en Ingeniería Aeroespacial	
142-Degree in Design Engineering	Grado en Ingeniería en Diseño y Desarrollo de Productos	
161-Degree in Electrical Engineering	Grado en Ingeniería Eléctrica	
163-Degree in Electronic Engineering	Grado en Ingeniería Electrónica Industrial y Automática	
169-Degree in Mechanical Engineering	Grado en Ingeniería Mecánica	
2160-Master Degree in Design Engineering	Master Universitario en Ingeniería del Diseño	
2165-Master Degree in Sensors for Industrial Application	Master Universitario en Sensores para Aplicaciones Industriales	
2187-Master in Mechatronics	Master Universitario en Ingeniería Mecatrónica	
2237-Master in Aeronautical Engineering	Master Universitario en Ingeniería Aeronáutica	
Possibility of attending courses from different Schools	Yes, up to 12 credits/semester or 2 courses/semester	
Staff Details at ETSID		
Head of International Office	Sergio Hoyas Calvo relint@etsid.upv.es Tel. +34 96 387 70 07 Ext. 71845 Fax: +34 96 387 91 80	
International Office Manager	Manuel Fernández-Peña manferpe@upvnet.upv.es Tel. +34 96 387 70 07 Ext. 81815 Fax: +34 96 387 91 80	
Administrative IO	Cristina Petit-Oltra relint@etsid.upv.es Tel. +34 96 387 91 85 Fax: +34 96 387 91 80	



General Information	
Application Information	The application form, as well as instructions about how to complete it, can be found at: http://www.upv.es/entidades/OPII/infoweb/pi/info/818867normali.html For further information about Erasmus at UPV: http://www.upv.es/entidades/OPII/infoweb/pi/info/818854normali.html
Application deadline: Winter Semester/Academic year	From 1 st of April to 31 st of May
Application deadline: Spring Semester	From 15 th September to 31 st of October
Language of Instruction	In general, classes are taught in Spanish. However Schools also offer courses in English http://www.opii.upv.es/enlaces_internos/docencia_ingles.asp Degree in Electronic Engineering can be attended fully in English.
Academic Calendar	Winter Semester: early September to end January Spring Semester: early February to middle June

Important Information	
Application Process	All students must be nominated by their home University (relint@etsid.upv.es). Then students will have to apply online. http://www.upv.es/entidades/OPII/infoweb/pi/info/818867normali.html After the on-line registration, students will have to print the application and have it signed and stamped by the Erasmus Coordinator. Then, please, upload the application together with following documents: -Copy of the transcript of records -1 passport-sized photos -Spanish Language certificate (A2 level required) -Learning agreement (Erasmus students)
Accommodation	UPV does not arrange accommodation. A mentor student will be assigned to each accepted applicant for assistance if requested in the on-line application. Also, you can visit following sites on accommodation possibilities: http://www.upv.es/perfiles/estudiante/alojamientos-upv-en.html http://www.upv.es/pls/oalu/sic_pisos.busca_piso?p_idioma=i