



Full Name of Institution		Universitat Politècnica de València http://www.upv.es
Erasmus Code	E VALENCI02	
School	School of Design Engineering (ETSID) http://www.etsid.upv.es/Index.php?lang=en http://www.etsid.upv.es/	
Degrees offered at ETSID		
http://www.etsid.upv.es/?page_id=29 http://www.etsid.upv.es/?page_id=75		
Degree in Aerospace Engineering	Grado en Ingeniería Aeroespacial	
Degree in Design Engineering	Grado en Ingeniería en Diseño y Desarrollo de Productos	
Degree in Electrical Engineering	Grado en Ingeniería Eléctrica	
Degree in Electronic Engineering	Grado en Ingeniería Electrónica Industrial y Automática	
Degree in Mechanical Engineering	Grado en Ingeniería Mecánica	
Degree in Industrial Management	Licenciado en Organización Industrial	
Master Degree in Design Engineering	Master Universitario en Ingeniería del Diseño	
Master Degree in Sensors for Industrial Application	Master Universitario en Sensores para Aplicaciones Industriales	
Schedules: http://www.etsid.upv.es/?page_id=3564		
Possibility of attending courses from different Schools	Yes, up to 12 credits/semester or 2 courses/semester	
Staff Details at ETSID		
Head of International Office	Luis Manuel Sánchez-Ruiz relint@etsid.upv.es Tel. +34 96 387 70 07 Ext. 71845 Fax: +34 96 387 91 80	
International Office Manager	Manuel Fernández-Peña manferpe@upvnet.upv.es Tel. +34 96 387 70 07 Ext. 81815 Fax: +34 96 387 91 80	
Administrative	Cristina Petit-Oltra relint@etsid.upv.es Tel. +34 96 387 91 85 Fax: +34 96 387 91 80	



General Information	
Application Information	<p>The application form, as well as instructions about how to complete it, can be found at: http://www.opii.upv.es/incomings/</p> <p>For further information about Erasmus at UPV: http://www.upv.es/entidades/OPII/infoweb/pi/info/758347normali.html</p>
Application deadline: Semester 1/Academic year	From 1 st of April to 31st of May
Application deadline: Semester 2	From 1st April to 31st of October
Language of Instruction	<p>In general, classes are taught in Spanish. However Schools also offer courses in English http://www.opii.upv.es/enlaces_internos/docencia_ingles.asp</p> <p>Degree in Electronic Engineering can be attended fully in English.</p>
Academic Calendar	<p>Semester 1: early September to end January Semester 2: early February to middle June</p> <p>Please, find academic calendar in the attachment</p>

Important Information	
Application Process	<p>All students must be nominated by their home University (relint@etsid.upv.es).</p> <p>Then students will have to apply online. http://www.opii.upv.es/incomings/</p> <p>After the on-line registration, students will have to print the application and have it signed and stamped by the Erasmus Coordinator.</p> <p>The application has to be sent by regular post together with following documents:</p> <ul style="list-style-type: none">-Copy of the transcript of records-1 passport-sized photos-Spanish Language certificate (at least A1 level required)-Learning agreement (Erasmus students) <p>Please, send the application documents to following address:</p> <p>International Relations Office School of Design Engineering (ETSID) Universitat Politècnica de València Cno. Vera s/n 46022 Valencia, Spain</p>



Accommodation	<p>UPV does not arrange accommodation. A mentor student assigned to each accepted applicant for assistance.</p> <p>Also, you can visit following sites on accommodation possibilities:</p> <p>http://www.upv.es/perfiles/estudiante/alojamientos-upv-en.html</p> <p>http://www.upv.es/pls/oalu/sic_pisos.busca_piso?p_idioma=i</p>
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